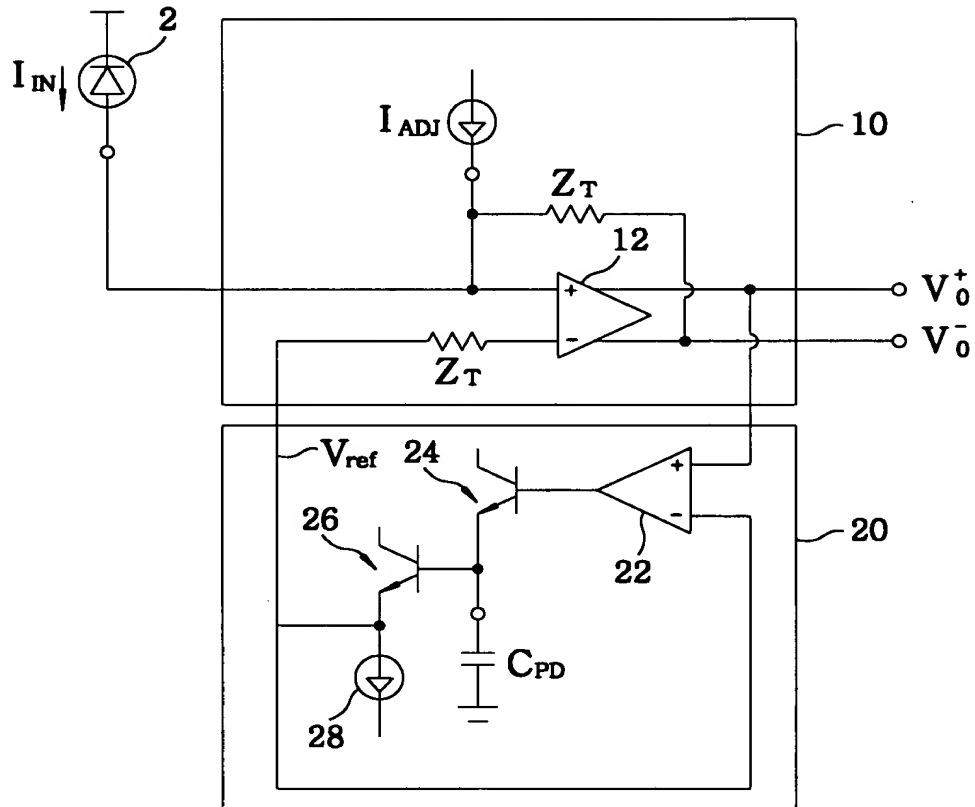
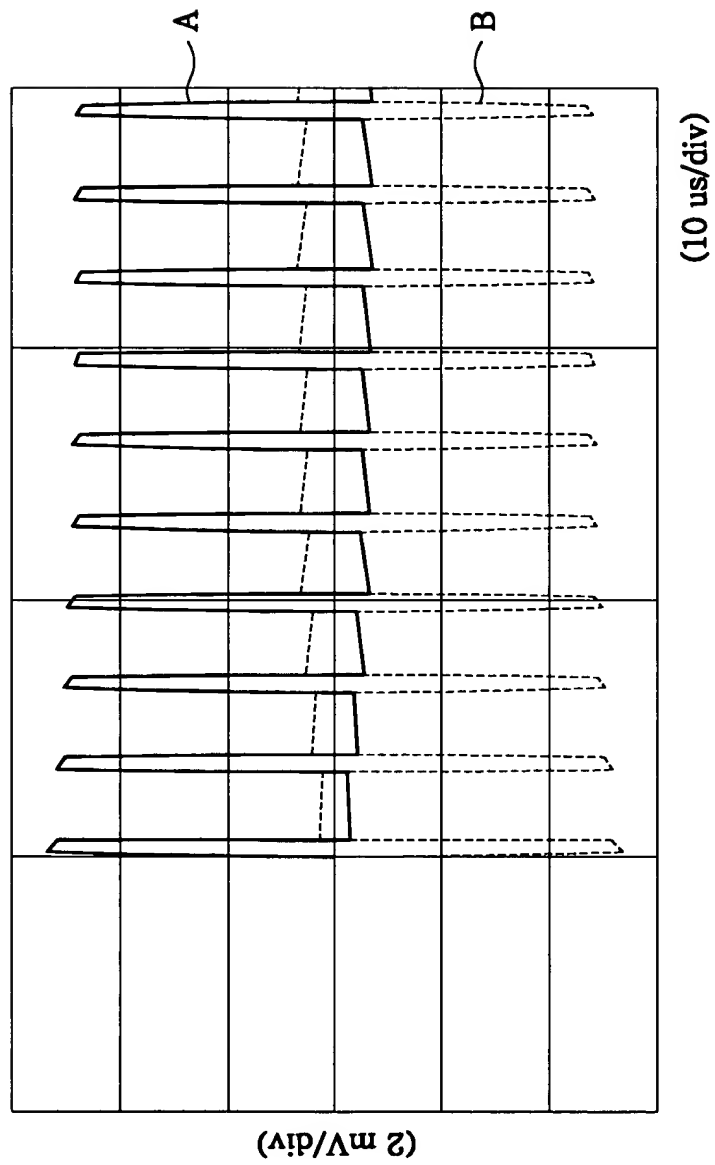


**FIG. 1**  
(PRIOR ART)



**FIG.2**  
(PRIOR ART)



**FIG. 3**

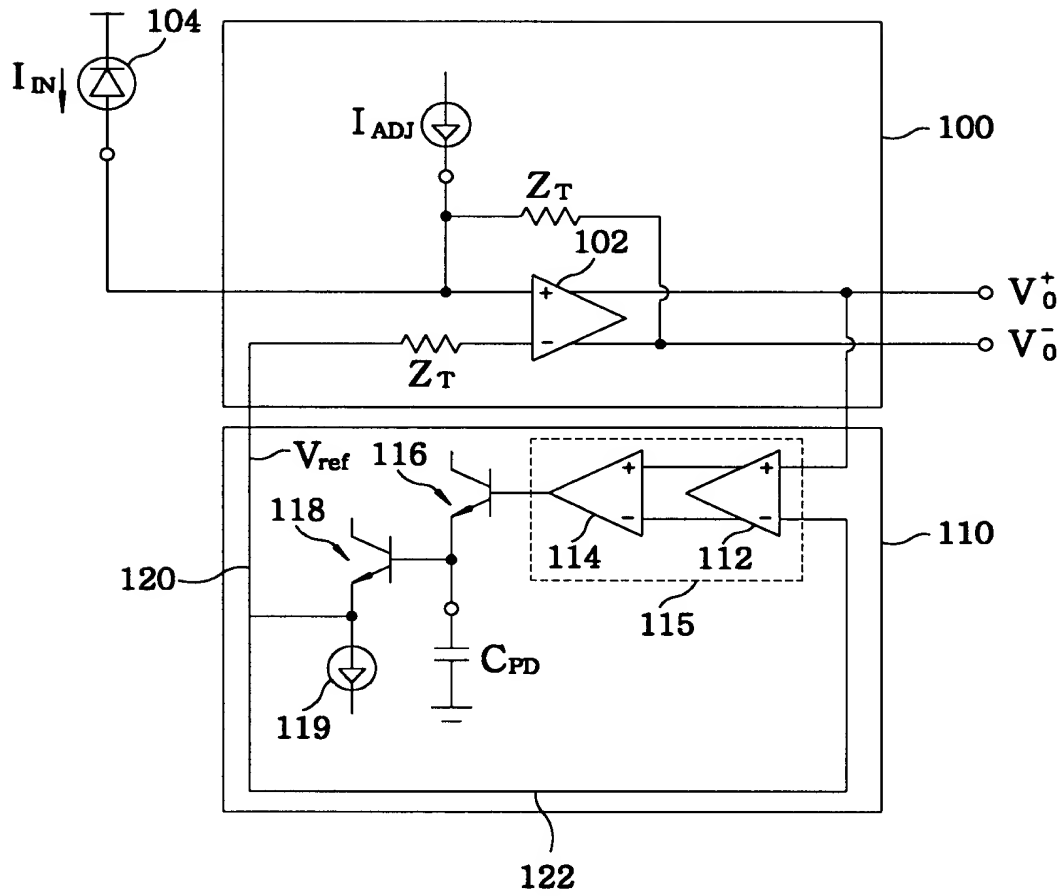
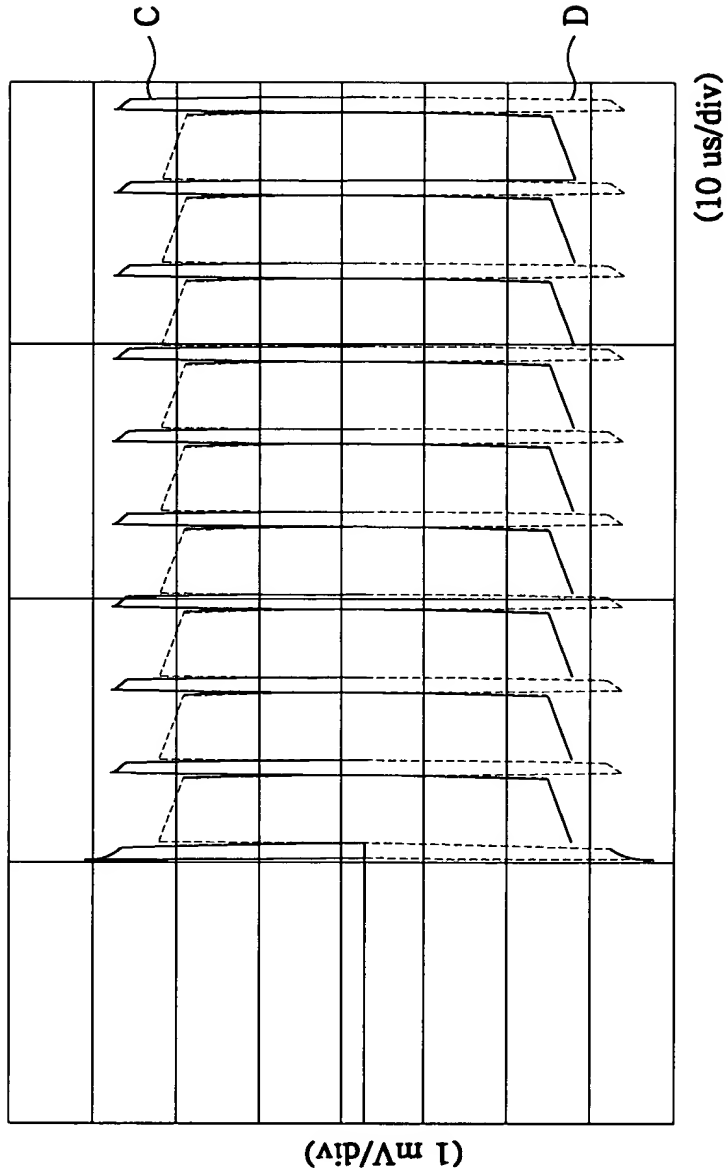


FIG. 4 is a graph showing the relationship between the input voltage and the output voltage of the circuit. The graph is plotted on a grid with a vertical axis labeled (1 mV/div) and a horizontal axis labeled (10 us/div). The graph shows a series of pulses, with the first pulse labeled C and the second pulse labeled D. The pulses are shown as solid lines, and the dashed lines represent the ideal response. The graph shows that the circuit is able to follow the input voltage, but there is a slight delay in the response.

FIG. 4



# FIG. 5

